



DIS Program Review 2001



Algorithmic Strategies for Compiler Controlled Caches (ASC³)

DARPA F33165-99-1-1499

September 19, 2001

7:30 am	8:00 am	Assembly and Continental Breakfast in CREST Room #228, 778 Atlantic Dr,
8:00 am	8:20 am	Introduction and Program Status, <i>Krishna V. Palem (Georgia Tech)</i>
8:20 am	8:40 am	Cache Sensitive Instruction Scheduling, <i>Charles R. Hardnett (Georgia Tech)</i>
8:40 am	9:00 am	Markov Prefetchers and Programmable Cache Management, <i>Jinwoo Kim (Georgia Tech)</i>
9:00 am	9:15 am	Trimaran Status and Demo, <i>Lakshmi N. Chakrapani (Georgia Tech), Yogesh Chobe (George Washington University)</i>
9:15 am	9:30 am	BREAK
9:30 am	10:10 am	Data Remapping for Locality Enhancement, <i>Rodric M. Rabbah (Georgia Tech)</i>
10:10 am	10:35 am	Discussion and Implications To Programmable Caches, <i>Krishna V. Palem (Georgia Tech) and Rodric M. Rabbah (Georgia Tech)</i>
10:35 am	11:00 am	From Programmable to Application Specific Caches, <i>Krishna V. Palem (Georgia Tech)</i>
11:00 am	11:10 am	BREAK
11:10 am	11:45 am	Randomized Strategies for Cache Optimization and On-Chip Parallelism, <i>Michael O. Rabin (Harvard University)</i>
11:45 am	12:00 pm	Discussion, Direction and Feedback, <i>Dr. Robert Graybill (DARPA) and team</i>
12:00 pm		Meeting concludes